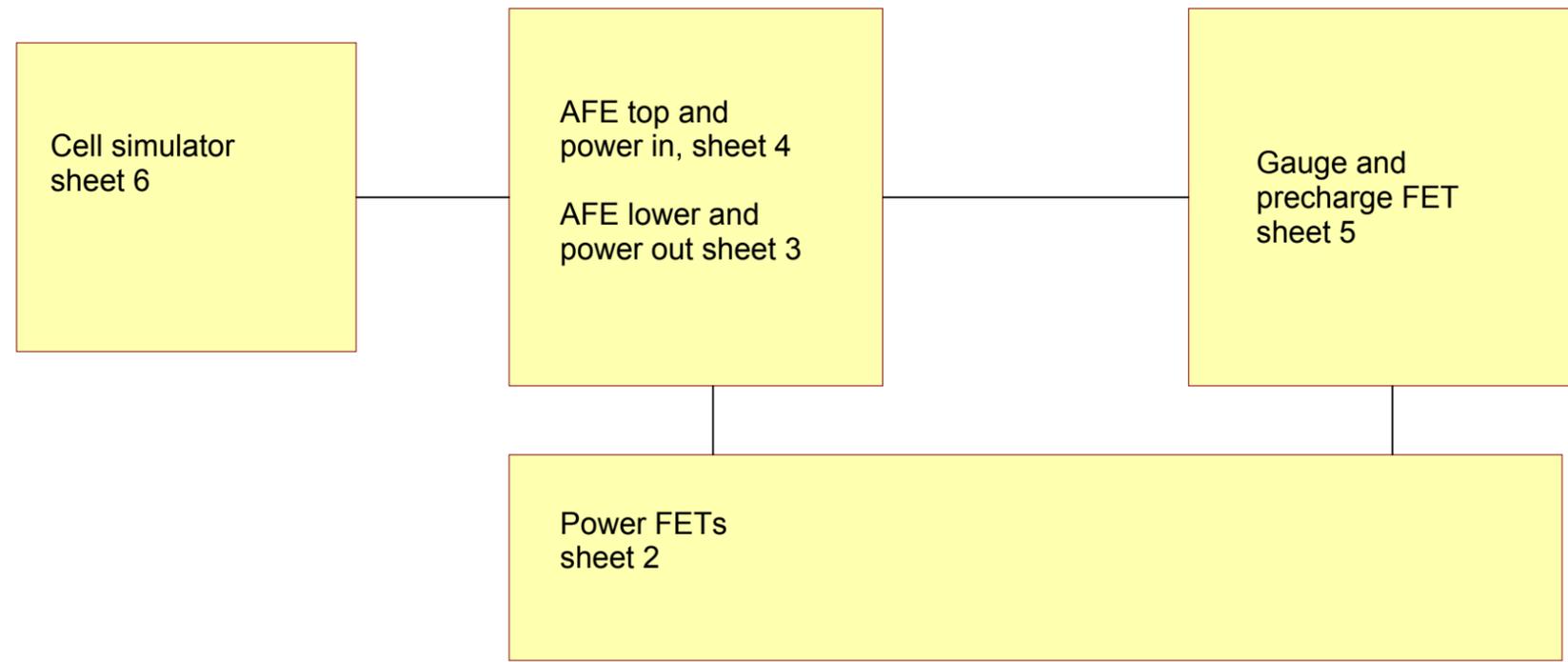


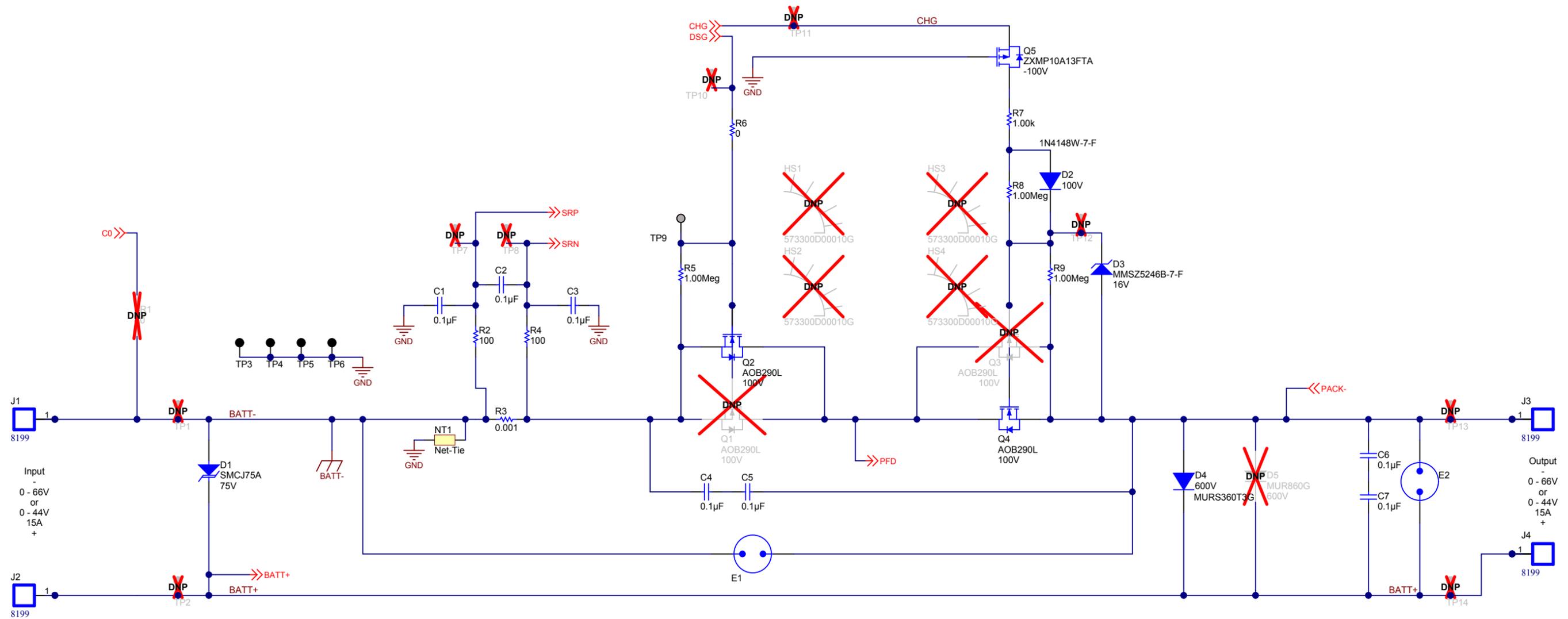
Revision History	
Revision	Notes



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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 11/12/2013
SVN Rev: Not in version control	Assembly Variant: -001	Project Title: bq76930/940 EVM	Sheet: 1 of 7
Drawn By: Willy Massoth	File: PWR524A_Cover.SchDoc	Sheet Title: Cover	Size: B
Engineer: W Massoth	Contact: http://www.ti.com/support		



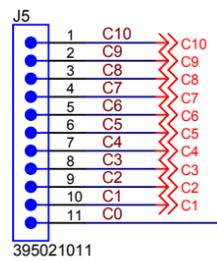
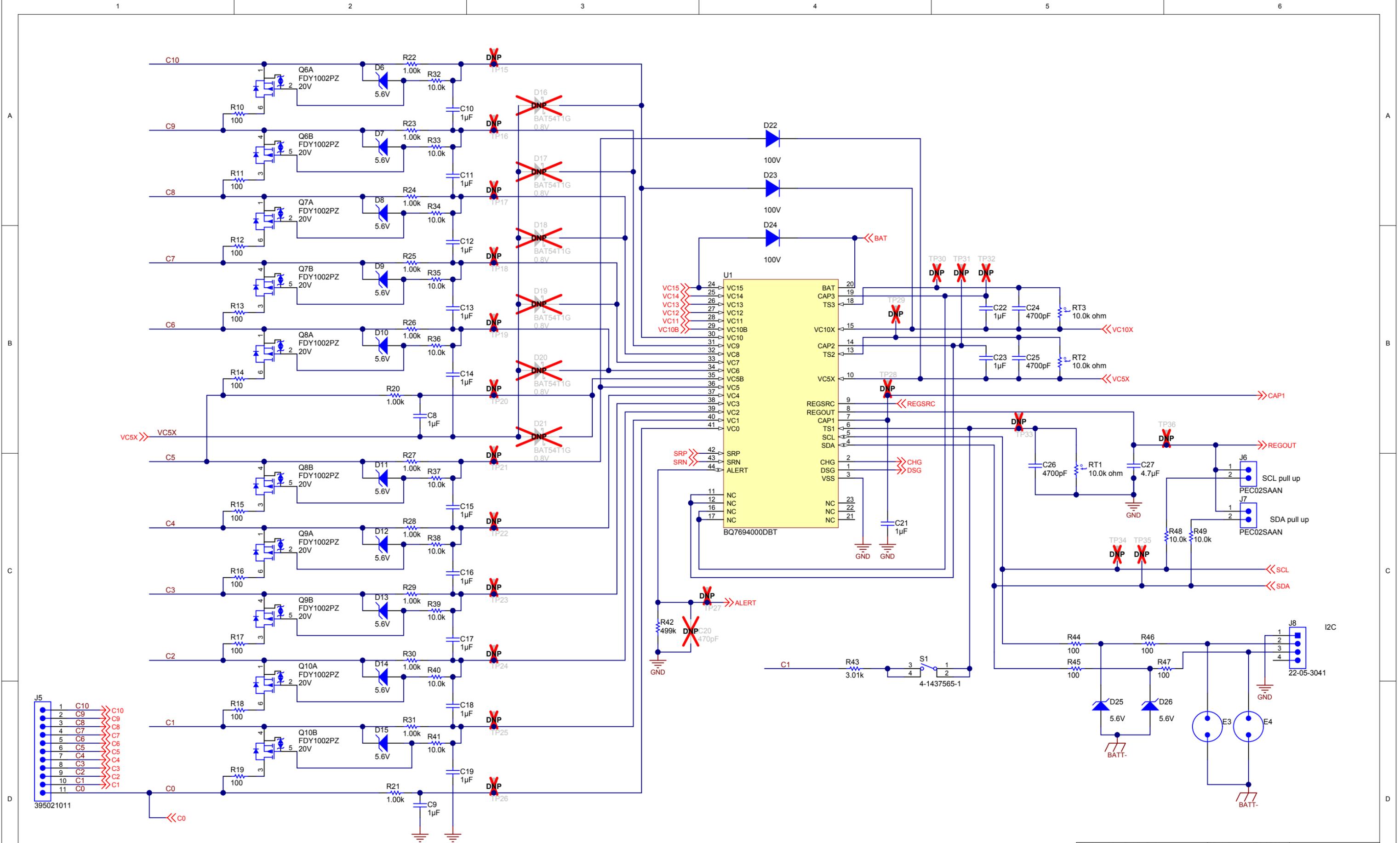


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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 1/22/2014
SVN Rev: Not in version control	Sheet Title: AFE	Project Title: bq76930/940 EVM	
Drawn By: Willy Massoth	File: PWR524A_FETs.SchDoc	Assembly Variant: -001	Sheet: 2 of 7
Engineer: W Massoth	Contact: http://www.ti.com/support		Size: B



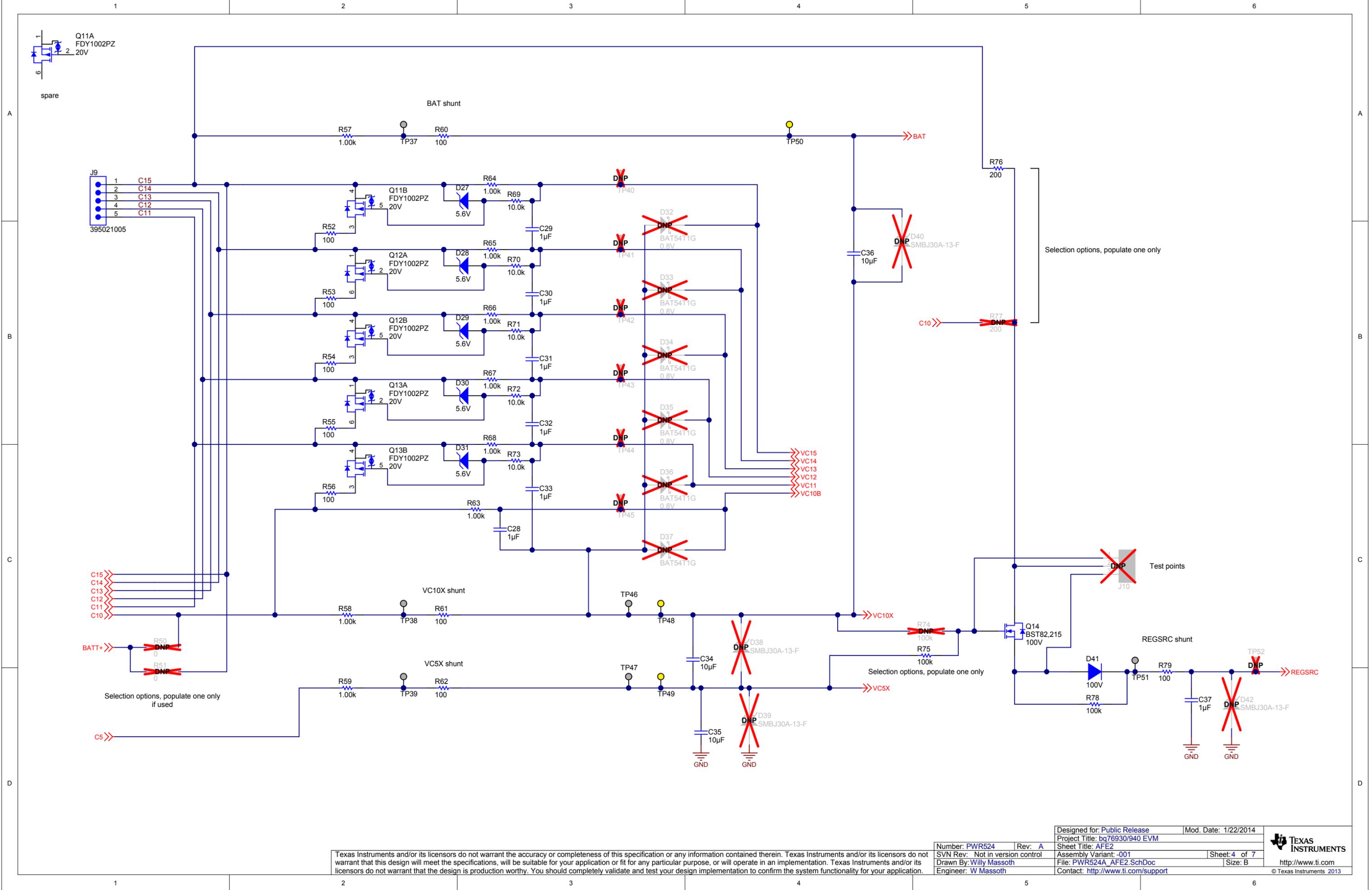
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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 1/22/2014
SVN Rev: Not in version control	Sheet Title: AFE1	Project Title: bq76930/940 EVM	Sheet: 3 of 7
Drawn By: Willy Massoth	File: PWR524A_AFE1_SchDoc	Assembly Variant: -001	Size: B
Engineer: W Massoth	Contact: http://www.ti.com/support		

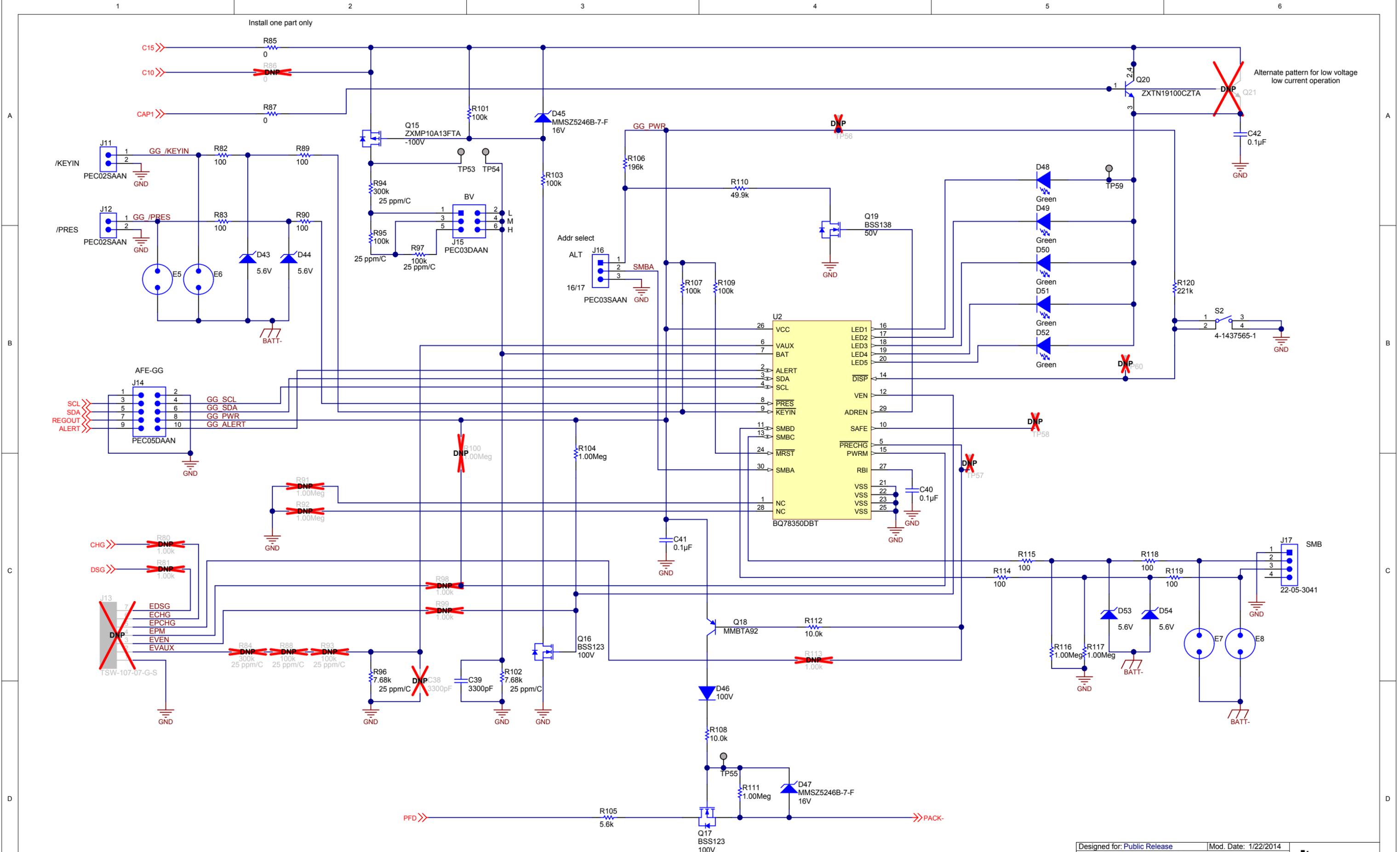




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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 1/22/2014
SVN Rev: Not in version control	Sheet Title: AFE2	Project Title: bq76930/940 EVM	
Drawn By: Willy Massoth	File: PWR524A_AFE2.SchDoc	Assembly Variant: -001	Sheet: 4 of 7
Engineer: W Massoth	Contact: http://www.ti.com/support	Size: B	

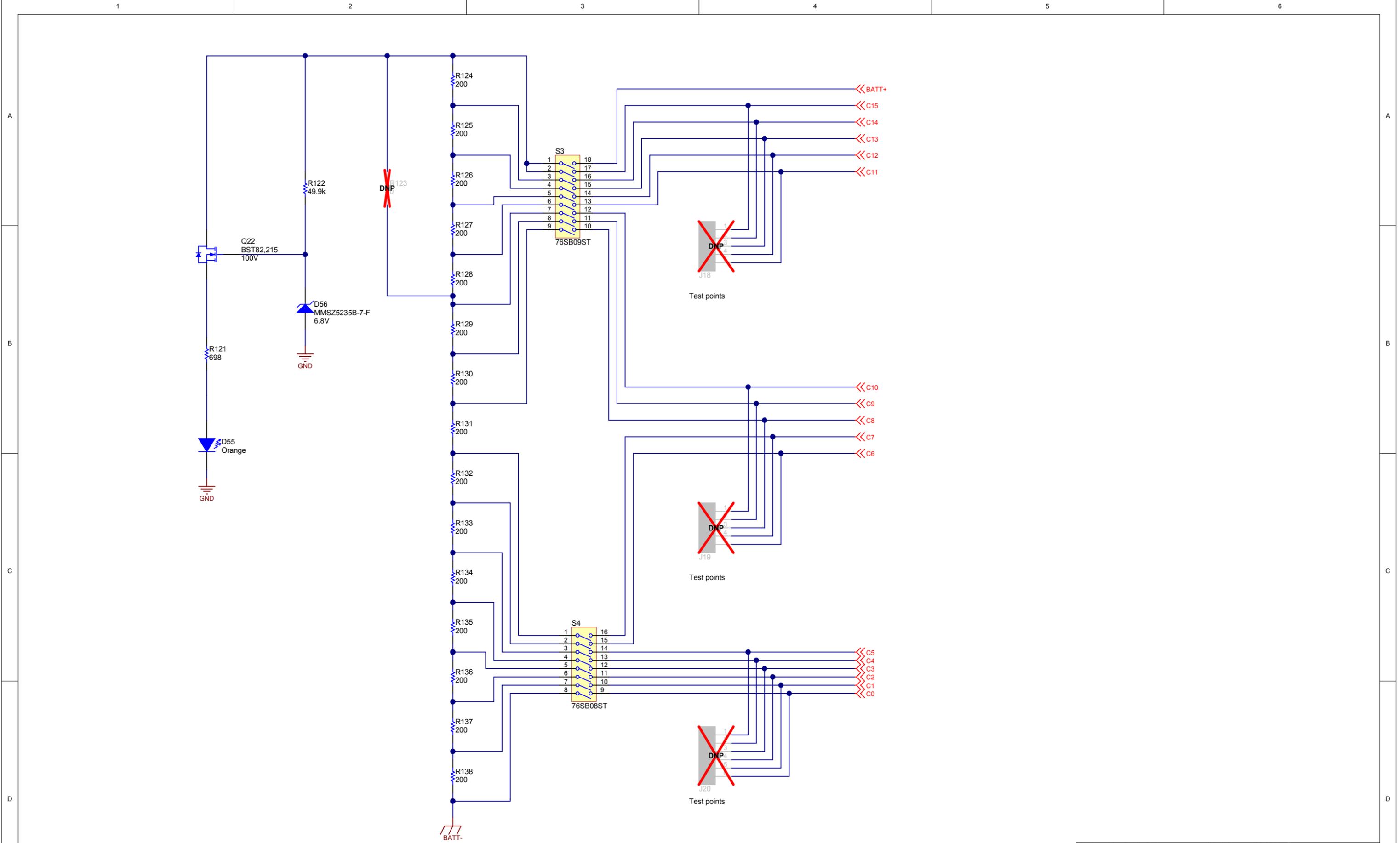
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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 1/22/2014
SVN Rev: Not in version control		Project Title: bq76930/940 EVM	
Drawn By:		Sheet Title: Gauge	
Engineer: W Massoth		Assembly Variant: -001	Sheet: 5 of 7
		File: PWR524A_BQ78350DBT_SchDoc	Size: B
		Contact: http://www.ti.com/support	

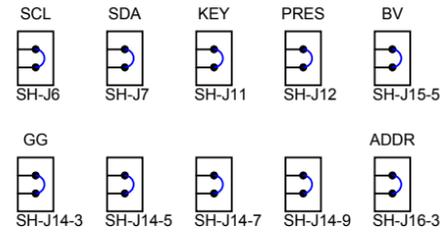




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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 1/22/2014
SVN Rev: Not in version control	Assembly Variant: -001	Project Title: bq76930/940 EVM	Sheet Title: Cell simulator
Drawn By:	Engineer: W Massoth	File: PWR524A_sim.SchDoc	Sheet: 6 of 7
		Contact: http://www.ti.com/support	Size: B





P1
MECH
39500-0011

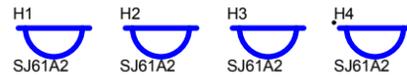
P2
MECH
39500-0005

W1
MECH
CBL002



PCB Number: PWR524
PCB Rev: A

PCB
LOGO
Texas Instruments



LBL1
PCB Label
Size: 0.65" x 0.20"

ZZ1
Label Assembly Note
Install label in silkscreen box after final board wash. Text shall be 8 pt. font. Text shall be variant name

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2., unless otherwise specified.

~~ZZ5
DNP
Mount U1 to align with pin 1 of pattern~~

ZZ6
Assembly Note
Install P1 and P2 on mating headers after final board wash

ZZ7
Assembly Note
Install shunts on referenced headers after final board wash

ZZ8
Assembly Note
Install adhesive feet per assembly variant

PCB
LOGO
DANGER HIGH VOLTAGE

PCB
LOGO
DANGER HIGH VOLTAGE

PCB
LOGO
CAUTION HOT SURFACE

PCB
LOGO
CAUTION HOT SURFACE

PCB
LOGO
DANGER HIGH VOLTAGE

PCB
LOGO
CAUTION. READ USER GUIDE BEFORE USE

PCB
LOGO
CAUTION. DO NOT REACH UNDER BOARD

LOGO4
 DANGER HIGH VOLTAGE

LOGO10
 DANGER HIGH VOLTAGE

LOGO11
 DANGER HIGH VOLTAGE

LOGO12
 DANGER HIGH VOLTAGE

LOGO13
 DANGER HIGH VOLTAGE

LOGO14
 DANGER HIGH VOLTAGE

LOGO15
 CAUTION HOT SURFACE

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Number: PWR524	Rev: A	Designed for: Public Release	Mod. Date: 2/21/2014
SVN Rev: Not in version control	Sheet Title: Hardware	Project Title: bq76930/940 EVM	
Drawn By: Willy Massoth	File: PWR524A_Hardware.SchDoc	Assembly Variant: -001	Sheet: 7 of 7
Engineer: W Massoth	Contact: http://www.ti.com/support		Size: B

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